

IOSR Journal of VLSI and Signal Processing (IOSR-JVSP)

Managing Editor Board

- Dr. B. D. Venkatraamana Reddy Dept.of ECE, MITS, A.P, India
- ✤ Dr. Mohsin khan Rural Education and Development Foundation, Pakistan
- ✤ Dr. S.Sasikumar ANNA university, India
- ✤ Dr. P. Karthigaikumar Karunya University, India
- Dr. Rania Ahmed jazan university, Egypt
- Dr. Roukhe Hassane Ismac, Malaysia
- Dr. Aleksandr Cariow West Pomeranian University of Technology, Szczecin, Poland
- Dr. Swapnadip De Jadavpur University, India

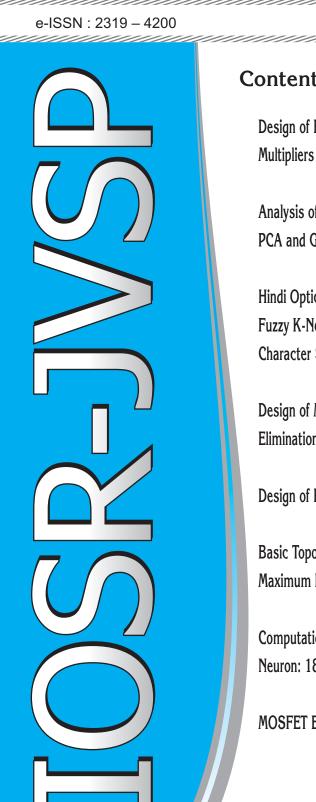
Contact Us

Website URL : www.iosrjournals.org Email : Support@iosrmail.org



IOSR Journals International Organization

of Scientific Research



Content

Design of I Multipliers

Analysis of PCA and G

Hindi Optic Fuzzy K-Ne Character

Design of l Elimination

Design of I

Basic Topo Maximum

Computation Neuron: 18

MOSFET E

IOSR Journal of WLSI and Signal Processing

Volume:8 Issue:1	p-ISSN : 2319	
ts :		
RSA Processor and Field Arithmetic of ECC s for Nodes in Wireless Sensor Networks	with Vedic	01-15
of the Performance of Classifiers on Wavelet GA for the Detection of Breast Cancer in Ultr		16-24
ical Character Recognition For Printed Docur learest Neighbor Algorithm: A Problem Appr Segmentation	·	25-34
Modified Vertical-Horizontal Binary Common n Algorithm Based On CBL forFIR Filter App	•	35-42
Read and Write Operations for 6t Sram Cell		43-46
ologiesf Mos Single-Stage Amplifiers. Dc An Input-Voltage Swing And Amplification	alysis For	47-59
ion and Analysis of Excitatory Synapse and I 80nm MOSFET and CNFET Technology	ntegrate & Fire	60-72
EKV Verilog-A Model Implementation in Gen	esys	73-86